Abstract of the Disclosure

A Verilog/PLI module that simulates behavior of an electronic system is co-simulated in a clock-accurate manner with a System C module that models the behavior of a component of the electronic system using a remote procedure call (RPC). Adding RPC functionality to the Verilog/PLI and System C modules facilitates exchanging parameters between the modules. The RPC is used to transfer process control to the SystemC module, after execution of which control is returned to the Verilog/PLI module. A return value is also returned to the Verilog/PLI module; this return value represents output signals associated with the System C module.

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